PATENT ABSTRACTS OF JAPAN

(11)Publication number : 06-250919

(43)Date of publication of application : 09.09.1994

(51)Int.Cl.   G06F 12/02

(21)Application number : 06-005612

(22)Date of filing :   24.01.1994

(71)Applicant : INTERNATL BUSINESS MACH CORP <IBM>

(72)Inventor : DUVALSAINT KARL JEAN
GUM PETER HERMON
KIM MOON JU
KRUMM BERRY WATSON
MCCAULEY DONALD WILLIAM
SCANLON JOHN FENTON

(30)Priority
Priority number : 93 14977   Priority date : 08.02.1993   Priority country : US

(54) COMPUTER MEMORY SYSTEM

(57)Abstract:
PURPOSE: To provide a memory reconstituting system in which a guest absolute storage space is mapped to plural discontinuous host absolute storage spaces.
CONSTITUTION: When a sufficient actual address designating capacity is not valid for providing a hall range in a host absolute address designating space, a storage to be assigned to a guest is reconstituted by a control program to be executed in the data processing system. A memory 13 is reconstituted by the control program, and the control program allows a main storage and extended storage related with the guest actual storage to be mapped to the plural discontinuous areas of a host absolute space. When the sufficient actual address designation is not valid in the host absolute address designating space, the control program enables the extension of the host absolute storage space to which the guest storage is mapped.